

PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: 023-0030
	Application No.: 10/729,844
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	First Named Inventor: <i>En-Hsing Chen</i>
	Filing Date: December 5, 2003
	Group Art Unit: 2827
	Examiner Name: Mai, Son Luu
Sheet 1 of 1	Date Submitted: June 29, 2005

**\*EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

**1'Applicant's unique citation designation number (optional):**

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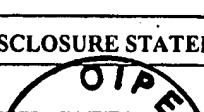
<sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3)

\*For Japanese patent documents, the indication of the year of the reign of the Emperor

*For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.*

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PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: 023-0030
	Application No.: 10/729,844
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	First Named Inventor: Roy E. Scheuerlein
	Filing Date: December 5, 2003
	Group Art Unit: 2818-2827
	Examiner Name: Son Luu Mai
Sheet 1 of 1	Date Submitted: June 1, 2005

## U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
<i>JM</i>		6,380,636	04-30-2002	Tatsukawa et al.	

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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
Examiner Signature		SON MAI		Date Considered	7/8/05	

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O P E R A T I O N INFORMATION DISCLOSURE STATEMENT BY APPLICANT APR 18 2005 PATENT & TRADEMARK OFFICE		First Named Inventor: En-Hsing Chen et al.
		Filing Date: December 5, 2003
		Group Art Unit: 2818-2827
		Examiner Name: Son Luu Mai
Sheet 1 of 2		Date Submitted: April 18, 2005

#### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
fm	AA	DURISETY, CHANDRA SEKHAR ACHARYULU, "Analysis and Characterization of Single-Poly Floating Gate Devices in 0.35um PDSOI Process", A Thesis Presented for the Master of Science Degree, The University of Tennessee, Knoxville, December 2002, pp. i-viii and 1-80.	/
fm	AB	EVANS, ROBERT J., ET AL., "Energy Consumption Modeling and Optimization for SRAM's," IEEE Journal of Solid-State Circuits, Vol. 30, No. 5, May 1995, pp. 571-579.	/
fm	AC	JUNG, TAE-SUNG, ET AL., "A 117-mm <sup>2</sup> 3.3-V Only 128-Mb Multilevel NAND Flash Memory for Mass Storage Applications," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1575-1583.	/
fm	AD	KAWAGOE, HIROTO, ET AL., "Minimum Size ROM Structure Compatible with Silicon-Gate E/D MOS LSI," IEEE Journal of Solid-State Circuits, Vol. SC-11, No. 3, June 1976, pp. 360-364.	/
fm	AE	KIM, KYU-HYOUN, ET AL., "An 8-Bit-Resolution, 360- $\mu$ s Write Time Nonvolatile Analog Memory Based on Differentially Balanced Constant-Tunneling-Current Scheme (DBCS)," IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, November 1998, pp. 1758-1762.	/
fm	AF	KIM, BYUNGCHEUL, ET AL., "A Scaled SONOS Single-Transistor Memory Cell for a High-Density NOR Structure with Common Source Lines," Journal of the Korean Physical Society, Vol. 41, No. 6, December 2002, pp. 945-948.	/
fm	AG	KIM, BYUNGCHEUL, ET AL., "Single Power Supply Operated and Highly Reliable SONOS EEPROMs," Journal of the Korean Physical Society, Vol. 40, No. 4, April 2002, pp. 642-644.	/
fm	AH	KITANO, YOSHITAKA, ET AL., "A 4-Mbit Full-Wafer ROM," IEEE Journal of Solid-State Circuits, Vol. SC-15, No. 4, August 1980, pp. 686-693.	/
fm	AI	LEE, YONG KYU, ET AL., "Multilevel Vertical-Channel SONOS Nonvolatile Memory on SOI," IEEE Electron Device Letters, Vol. 23, No. 11, November 2002, pp. 664-666.	/
fm	AJ	LIN, HORNG-CHIH, ET AL., "Ambipolar Schottky-Barrier TFTs," IEEE Transactions on Electron Devices, Vol. 49, No. 2, February 2002, pp. 264-270.	/
fm	AK	NAJEEB-UD-DIN ET AL., "Analysis of Floating Body Effects in Thin Film Conventional and Single Pocket SOI MOSFETs Using the GIDL Current Technique", IEEE Electron Device Letters, Vol. 23, No. 4, April 2002, pp. 209-211.	/
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fm	AM	PARK, KI-TAE, "Recent Non-Volatile Memory Device & Circuit Technology," Halo LSI Inc, (date unknown), 35 pages.	/
fm	AN	ROIZIN, YAKOV, ET AL., "Plasma-Induced Charging in Two Bit per Cell SONOS Memories," Tower Semiconductor Ltd., Migdal HaEmek 23105, Israel, (date unknown), 4 pages.	/

Examiner Signature	SON Mai	Date Considered	07/08/05
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: En-Hsing Chen et al.
(Continuation Sheet)		Filing Date: December 5, 2003
		Group Art Unit: 2818-2827
		Examiner Name: Son Luu Mai
Sheet 2 of 2		Date Submitted: April 18, 2005

#### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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HM	AT	THEAN, AARON, ET AL., "Flash memory: towards single-electronics," IEEE Potentials, October/November 2002, pp. 35-41.	
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Examiner Signature		SON MAI	Date Considered
			07/08/05



Attorney Docket No.: 023-0030  
Application No.: 10/729,844  
Applicant(s): En-Hsing Chen et al.  
Filing Date: December 5, 2003  
Group Art Unit: 2818 2822  
Date Submitted: April 18, 2005

### LIST OF PENDING U. S. PATENT APPLICATIONS

The following pending U.S. patent applications are listed for consideration by the Examiner.

- JK*
- 1) U.S. Patent Application No. 10,729,865, filed December 5, 2003, entitled "NAND Memory Array Incorporating Multiple Series Selection Devices and Method for Operation of Same," naming inventors En-Hsing Chen, Andrew J. Walker, Roy E. Scheuerlein, Sucheta Nallamothu, Alper Ilkbahar, Luca G. Fasoli and James M. Cleeves.
  - 2) U.S. Patent Application No. 10/729,843, filed December 5, 2003, entitled "Memory Array Incorporating Memory Cells Arranged in NAND Strings," naming inventors Luca G. Fasoli, Roy E. Scheuerlein, En-Hsing Chen, Sucheta Nallamothu, Maitreyee Mahajani, and Andrew J. Walker.
- JM*

<u>EXAMINER</u>	<u>DATE CONSIDERED</u>
SON MAI	07/06/05

*Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609III(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609III(C)(2) and return a copy of this page with the next communication.*

PTO/SB/08A (Substitute for form 1449/PTO)		Attorney Docket No.: 023-0030
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O I P E INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: En-Hsing Chen et al.
		Filing Date: December 5, 2003
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		Examiner Name: Son Luu Mai
Sheet 1 of 5		Date Submitted: April 18, 2005

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
SL	AA	4,142,176	02-27-1979	Dozier	
SL	AB	4,543,594	09-24-1985	Mohsen et al.	
SL	AC	4,602,354	07-22-1986	Craycraft et al.	
SL	AD	4,868,616	09-19-1989	Johnson et al.	
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SL	AJ	5,568,421	10-22-1996	Aritome	
SL	AK	5,621,683	04-15-1997	Young	
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SL	AM	5,703,382	12-30-1997	Hack et al.	
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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
SL	AN	JA 62-142363	06-25-1987		
SL	AO	JA 62-155568	07-10-1987		
SL	AP	JA-2001-358237	12-26-2001		
SL	AQ	JA-2002-280467	09-27-2002		
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(Continuation Sheet)		Filing Date: December 5, 2003
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Sheet 2 of 5		Date Submitted: April 18, 2005

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	BE					
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	BV					
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